SPECIFICATION ADDENDUM

For the AT-AI-16XE-10 (Revision B), AT-MIO-16XE-10 (Revision B), PCI-6031E (Revision A), PCI-6032E (Revision C), PCI-6033E (Revision A), PCI-MIO-16XE-10 (Revisions A-C), VXI-MIO-64E-1 (Revisions B-F), VXI-MIO-64XE-10 (Revisions A-E), PXI™-6030E (Revisions A-D), PXI-6031E (Revisions A-B)

The onboard calibration reference specification is incomplete in both the *Analog Input* and *Analog Output* sections of Appendix A, *Specifications* in the user manual for each of these products. The corrected specification, with the addition in *italics*, is shown below:

Stability

Onboard calibration reference

Level .... 5.000 V (±1.0 mV)
(*over full operating temperature,* actual value stored in EEPROM)